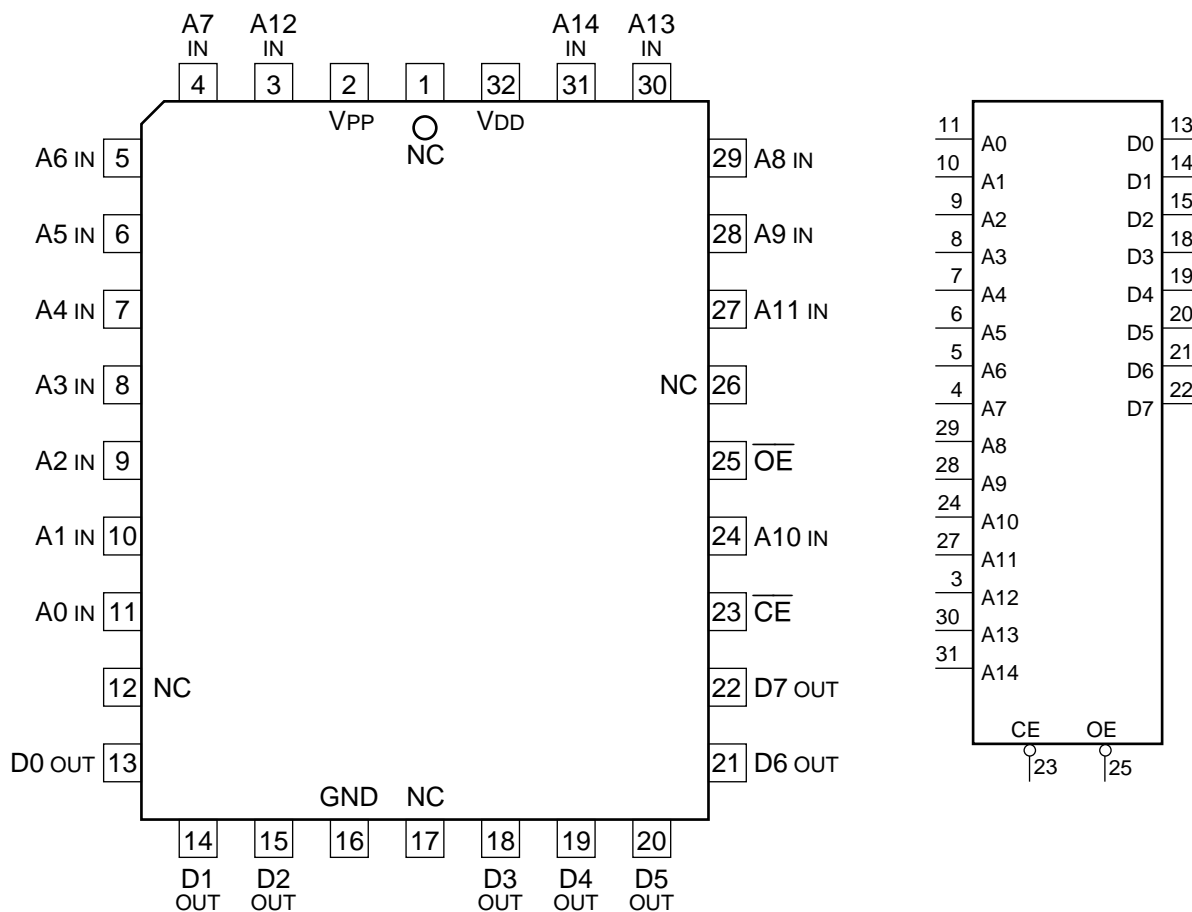


C-MOS 256 K (32 K × 8)-BIT EPROM

—TOP VIEW—



- A0 - A14 : ADDRESS INPUT
- \overline{CE} : CHIP ENABLE INPUT
- D0 - D7 : DATA OUTPUT
- \overline{OE} : OUTPUT ENABLE INPUT
- VPP : PROGRAM POWER SUPPLY

MODES	\overline{CE}	\overline{OE}	VPP	A9	D0 - D7
Read	0	0	+5 V	X	Dout
Program	0	1	+13 V	X	Din
Program Verify	1	0	+13 V	X	Dout
Program Inhibit	1	1	+13 V	X	HI-Z
Standby	1	X	+5 V	X	HI-Z
Output Disable	0	1	+5 V	X	HI-Z
Identity	0	0	+5 V	+13 V	Identity Code

- 0 : LOW LEVEL
- 1 : HIGH LEVEL
- X : DON'T CARE
- HI-Z : HIGH IMPEDANCE

